

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JPW

Application No.: 10/725,318
Filed: 12/2/2003
Inventor(s):
Katsuki Hazama

Examiner: Booth, Richard A.
Group/Art Unit: 2812
Atty. Dkt. No: 5957-82403



Title: SEMICONDUCTOR DEVICE
INCLUDING TRANSISTOR
WITH COMPOSITE GATE
STRUCTURE AND
TRANSISTOR WITH SINGLE
GATE STRUCTURE, AND
METHOD FOR
MANUFACTURING THE
SAME

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Dean M. Munyon

Printed Name

Dean M. Munyon
Signature

July 5, 2006

Date

RESPONSE TO NOTICE OF NON-COMPLIANT
AMENDMENT MAILED JUNE 5, 2006

M/S Amend
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This paper is submitted in response to the Notice of Non-Compliant Amendment of June 5, 2006, the Notice of Non-Compliant Amendment of March 8, 2006, and the Office Action of August 11, 2005, to further highlight why the application is in condition for allowance. The present amendment includes a complete listing of the claims.